



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Floman et al.

Application No.: 10/614,341

Group No.: 2133

Filed: July 2, 2003

Examiner: John J. Tabone, Jr.

For: **MEMORY BUS CHECKING PROCEDURE**

Mail Stop Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT TRANSMITTAL

1. Transmitted herewith is an amendment for this application.

STATUS

2. Applicant is
- ☐ a small entity. A statement:
- ☐ is attached.
- ☐ was already filed.
- ☒ other than a small entity.

CERTIFICATE OF MAILING/TRANSMISSION UNDER 37 C.F.R. §1.8(a)

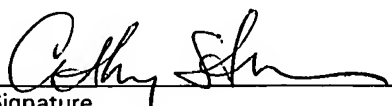
I hereby certify that this correspondence is, on the date shown below, being:

MAILING

☒ deposited with the United States Postal Service with sufficient postage as first-class mail, in an envelope addressed to the Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

FACSIMILE

☐ transmitted by facsimile to the U.S. Patent and Trademark Office.


Signature

Date: 12-3-04

Cathy Sturmer
(type or print name of person certifying)

EXTENSION OF TERM

NOTE: "Extension of Time in Patent Cases (Supplement Amendments) - If a timely and complete response has been filed after a Non-Final Office Action, an extension of time is not required to permit filing and/or entry of an additional amendment after expiration of the shortened statutory period.

If a timely response has been filed after a Final Office Action, an extension of time is required to permit filing and/or entry of a Notice of Appeal or filing and/or entry of an additional amendment after expiration of the shortened statutory period unless the timely-filed response placed the application in condition for allowance. Of course, if a Notice of Appeal has been filed within the shortened statutory period, the period has ceased to run." Notice of December 10, 1985 (1061 O.G. 34-35).

NOTE: See 37 C.F.R. §1.645 for extensions of time in interference proceedings, and 37 C.F.R. §1.550(c) for extensions of time in reexamination proceedings.

3. The proceedings herein are for a patent application and the provisions of 37 C.F.R. §1.136 apply.

(complete (a) or (b), as applicable)

- (a) ☒ Applicant petitions for an extension of time under 37 C.F.R. §1.136 (fees: 37 C.F.R. §1.17(a)(1)-(4)) for the total number of months checked below:

<u>Extension (months)</u>	<u>Fee for other than small entity</u>	<u>Fee for small entity</u>
<input checked="" type="checkbox"/> one month	\$ 110.00	\$ 55.00
<input type="checkbox"/> two months	\$ 400.00	\$200.00
<input type="checkbox"/> three months	\$ 920.00	\$460.00
<input type="checkbox"/> four months	\$1,440.00	\$720.00

Fee: \$ 110.00

If an additional extension of time is required, please consider this a petition therefor.

(check and complete the next item, if applicable)

- ☐ An extension for _____ months has already been secured. The fee paid therefor of \$ _____ is deducted from the total fee due for the total months of extension now requested.

Extension fee due with this request \$ _____

OR

- (b) ☐ Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

FEE FOR CLAIMS

4. The fee for claims (37 C.F.R. §1.16(b)-(d)) has been calculated as shown below:

(Col. 1)	(Col. 2)	(Col. 3)	SMALL ENTITY			OTHER THAN A SMALL ENTITY	
CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	ADDIT. RATE	FEE	OR	ADDIT. RATE	FEE
TOTAL:	MINUS 20	= 0	x \$9 =	\$		x \$18 =	\$
INDEP:	MINUS 3	= 0	x \$42 =	\$		x \$84 =	\$
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM			+ \$140 =			+ \$280 =	
			TOTAL ADDL. FEE	\$		TOTAL ADDL. FEE	\$ 0.00

WARNING: "After final rejection or action (§1.113) amendments may be made cancelling claims or complying with any requirement of form which has been made." 37 C.F.R. §1.116(a) (emphasis added).

(complete (c) or (d), as applicable)

(c) ☒ No additional fee for claims is required.

OR

(d) ☐ Total additional fee for claims required is \$_____.

FEE PAYMENT

5. ☐ Attached is a check in the sum of \$_____.

☐ Charge Account No. _____ the sum of \$_____. A duplicate of this transmittal is attached.

FEE DEFICIENCY

NOTE: If there is a fee deficiency and there is no authorization to charge an account, additional fees are necessary to cover the additional time consumed in making up the original deficiency. If the maximum six-month period has expired before the deficiency is noted and corrected, the application is held abandoned. In those instances where authorization to charge is included, processing delays are encountered in returning the papers to the PTO Finance Branch in order to apply these charges prior to action on the cases. Authorization to charge the deposit account for any fee deficiency should be checked. See the Notice of April 7, 1986 (1065 O.G. 31-33).

6. ☒ If any additional extension and/or fee is required, charge Account No. 23-0442

AND/OR

☒ If any additional fee for claims is required, charge Account No. 23-0442 .



Signature of Practitioner

Reg. No.: 40,061

Kenneth Q. Lao

Ware, Fressola, Van Der Sluys & Adolphson LLP

Telephone No.: (203) 261-1234

Bradford Green, Building Five

755 Main Street, P.O. Box 224

Customer No.: 004955

Monroe, CT 06468



IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: Floman et al. : Attorney Docket No.: 944-001.112

Serial No.: 10/614,341 : Examiner: John J. Tabone, Jr.

Filed: July 2, 2003 : Art Unit: 2133

For: MEMORY BUS CHECKING PROCEDURE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION (Paper No. 082004)

Sir:

This responds to the Non-Final Office Action, mailed August 25, 2004.

In the patent application, claims 1-33 are pending. In the office action, all pending claims are rejected.

At section 3 of the office action, claims 1-33 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Cedar et al.* (WO-02/15020 A2, hereafter referred to as *Cedar*), in view of *Coyle et al.* (U.S. Patent No. 6,473,871, hereafter referred to as *Coyle*).

I hereby certify that this correspondence is being deposited today, December 3, 2004, with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Cathy Sturmer

In rejecting claims 1 and 18, the Examiner states that *Cedar* teaches that a host determines the data bus width of the SD card by reading the information stored in the SD Card Configuration Register (SCR, p.15, last paragraph). The Examiner considers the “read” command contains a first bit pattern and the information indicating the bus width in the SD card contains a second bit pattern. The Examiner admits that *Cedar* fails to teach the second bit pattern has a predetermined relationship with a second bit pattern, but points to *Coyle* for teaching such a predetermined relationship.

It is respectfully submitted that *Coyle* discloses a method of testing data errors in transmission, wherein a received pattern is compared to the transmit pattern through a number of test cycles. If the received pattern does not correspond to the transmit pattern, then errors are said to occur in the transmission. (col.10, lines 29-38). *Coyle* does not disclose determining the bus width of a device that is connected to a host device. *Coyle* is concerned with signaling errors due to low signal-to-noise ratio and signal distortion. (col.2, lines 62-67). In particular, the bit pattern forwarded back to the first device from the second device is identical to the bit pattern received by the second device. As shown in Figure 1, the receiver end 224 stores the received pattern and, after gaining control of the bus 205, forwards it back to the driver end 222 at the first device. Accordingly, the bit pattern traverses the bus 205 twice, once in a forward direction and once, during loopback, in the reverse direction (col. 7, lines 42-50). Thus, according to *Coyle*, the predetermined relationship between the transmitted pattern and the received pattern is that these two patterns are identical.

If the corresponding patterns between transmission and reception in *Coyle* are applied to the “read” command signal and the information in the SCR as disclosed in *Cedar*, then the bit pattern indicating the bus width in the SCR is identical to the “read” command bit pattern. It is not clear how this information can be used to indicate the bus width of the SD card.

Even if the predetermined relationship between the second bit pattern and the first bit pattern is that the second bit pattern is complementary to the first bit pattern (a binary bit pattern, claim 4), the information stored in the SCR is also useless. Now the information in the SCR has a bit pattern complementary to the bit pattern of the “read” command. It is not clear how this information can be used to indicate the bus width of the SD card.

The above-mentioned two examples are used to show that the information indicating the bus width in the SD card cannot have a predetermined relationship with the “read” pattern. If the

information in the SCR does have a predetermined relationship with the "read" pattern, then the information cannot be used to indicate the bus width.

The Examiner also states that *Cedar* discloses that the host controller 51' generates a single stream of data which the host outputs in parallel through a plurality of data lines and is received by the SD card (p.14, lines 14-28). It is respectfully submitted that *Cedar* discloses that, after knowing the number of data lines in the SD card, a multiplexer with a single input and multiple outputs is used to send a data stream to the SD device in a multiplexed manner. In particular, a serial data stream, such as a one-bit wide stream, is transmitted in parallel through two or more data lines by alternately directing a fixed number of consecutive data bits at a time, such as one bit, through each of the multiple data lines in sequences, and then reconstructing the data bits from the multiple lines. (p.5, lines 16-21). For example, the serial data stream, as shown in Figure 10a, is transmitted in parallel through four data lines DAT0, DAT1, DAT2 and DAT3 in a multiplexed manner, as shown in Figure 10B. After reconstruction, the received data stream is shown in Figure 10c. *Cedar* does not disclose or even suggest sending through the data bus a certain bit pattern to a memory card and receiving through the same data bus a return bit pattern so that the data bus width can be determined based on the relationship between the sent bit pattern and the returned bit pattern.

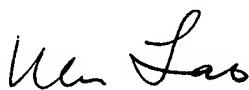
Thus, the cited *Cedar* and *Coyle* references are irrelevant to the claimed invention.

CONCLUSION

All pending claims are distinguishable over the cited references. Early allowance of these claims is earnestly solicited.

Respectfully submitted,

WARE, FRESSOLA, VAN DER SLUYS
& ADOLPHSON LLP
Bradford Green, Building Five
755 Main Street, P.O. Box 224
Monroe, CT 06468
Telephone: (203) 261-1234
Facsimile: (203) 261-5676
USPTO Customer No. 004955



Kenneth Q. Lao
Attorney for the Applicant
Registration No. 40,061